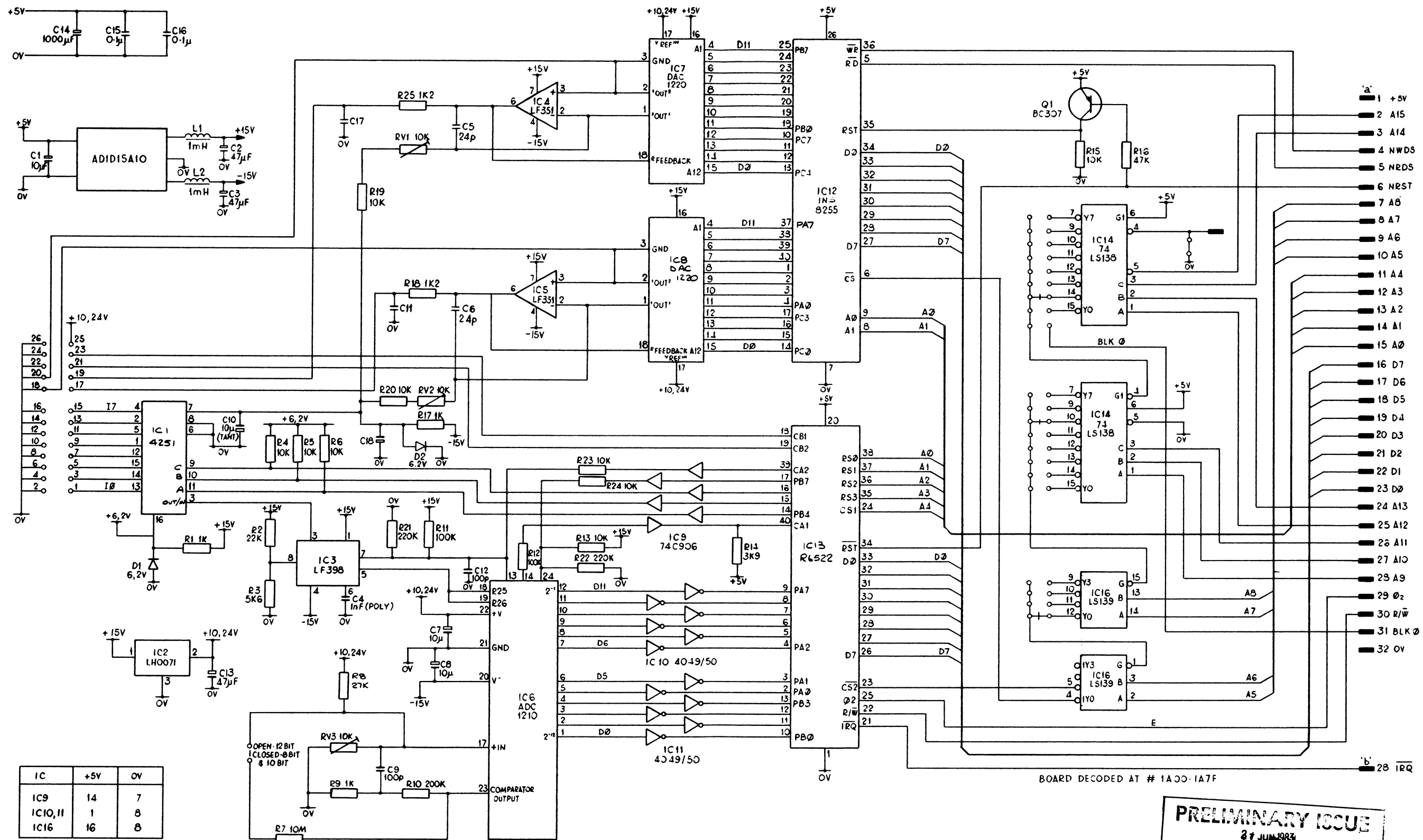


PRELIMINARY ISSUE
DATE 27 JUN 1983

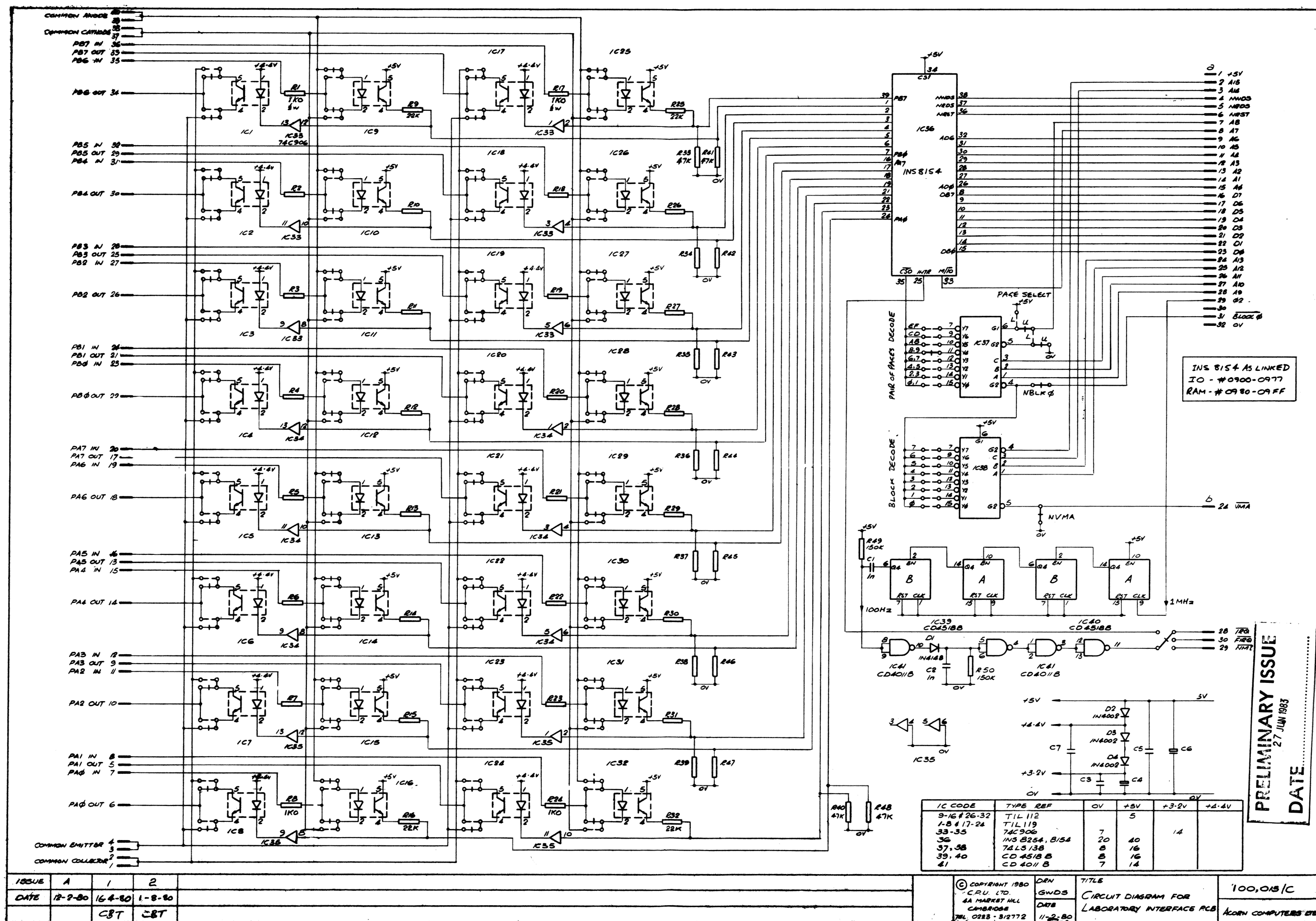


BOARD DECODED AT # 1A00-1A7F

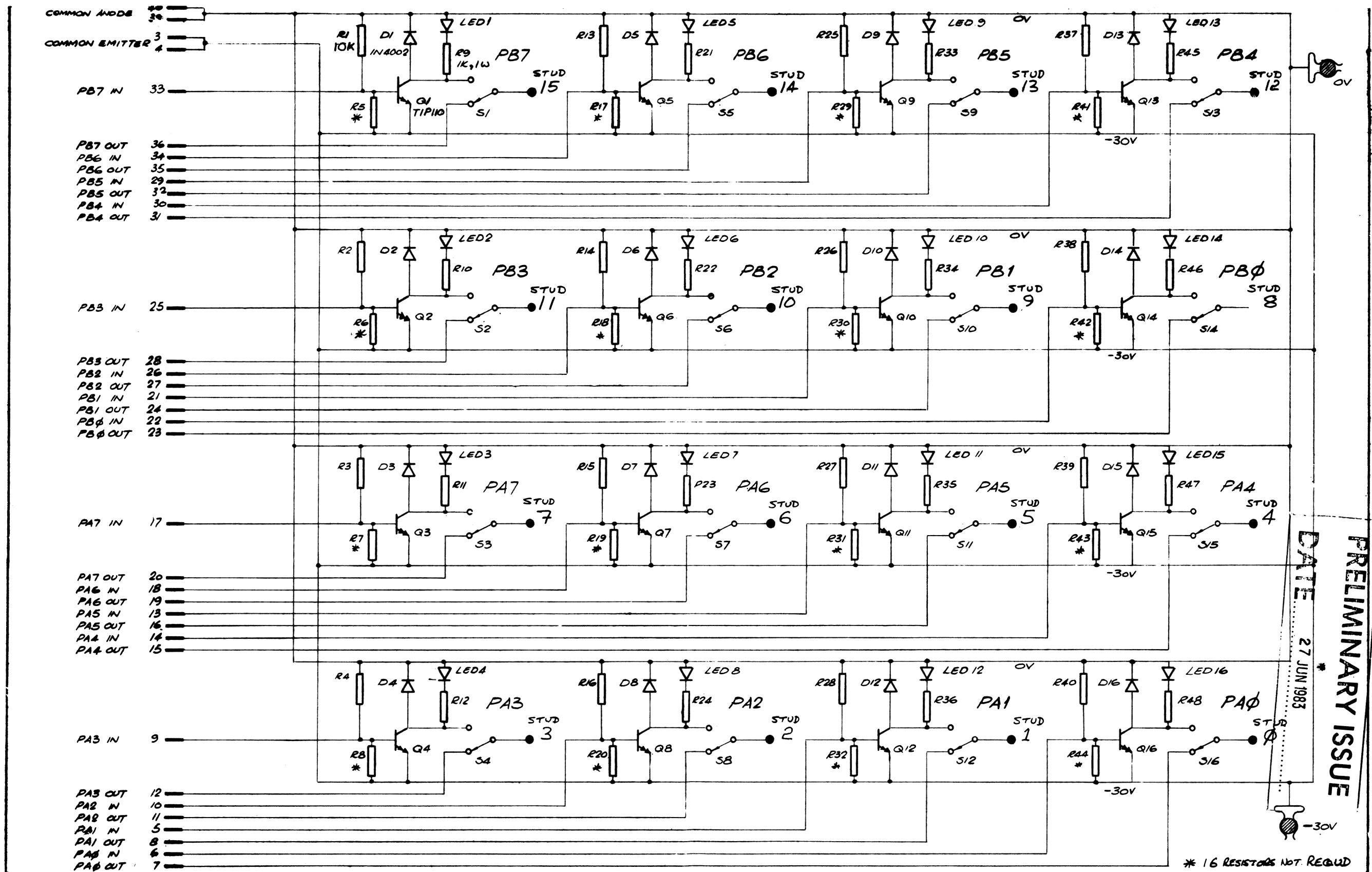
PRELIMINARY ISSUE

DATE 27 JUN 1983

[illegible]



DATE 27 JUN 1983 PRELIMINARY ISSUE

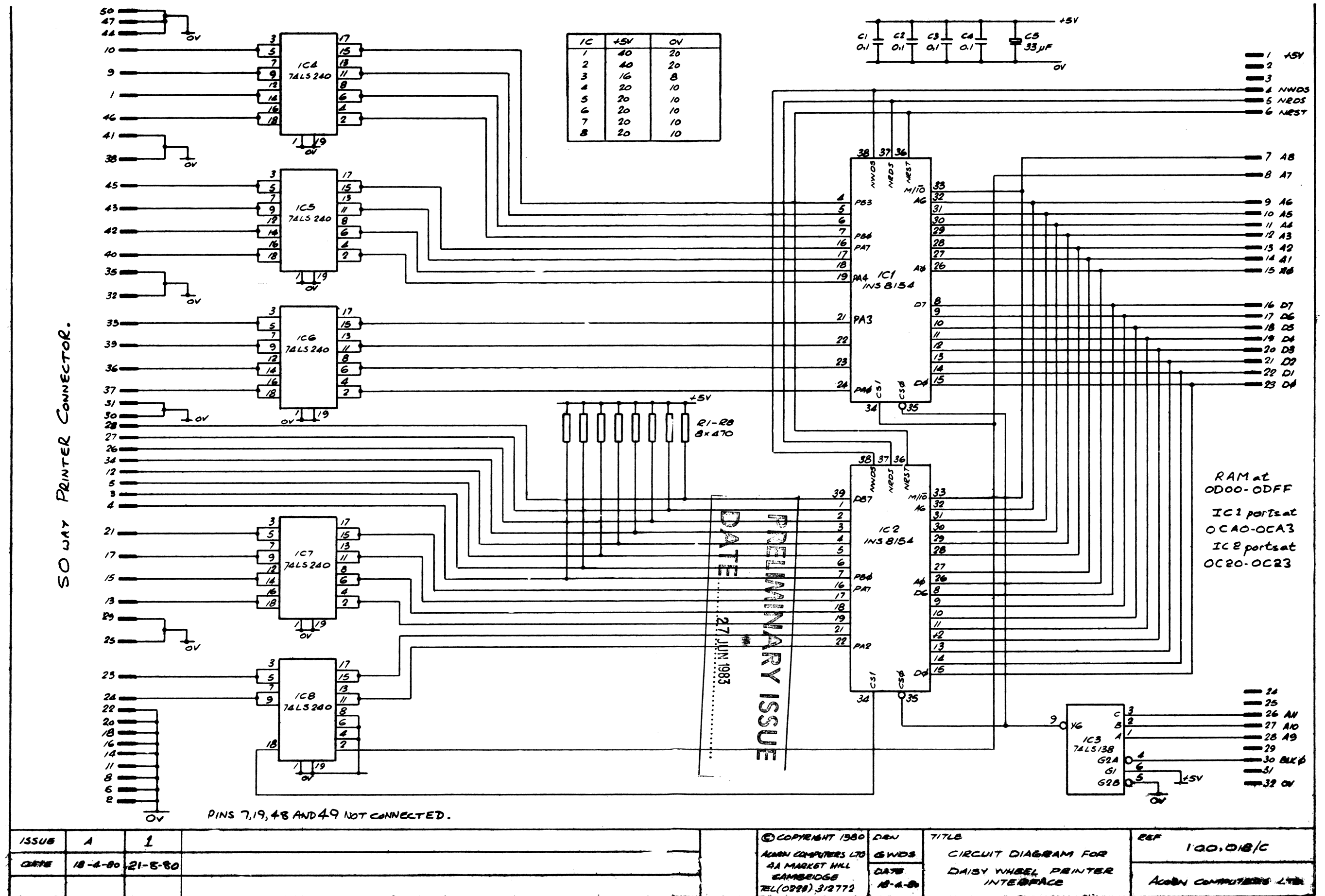


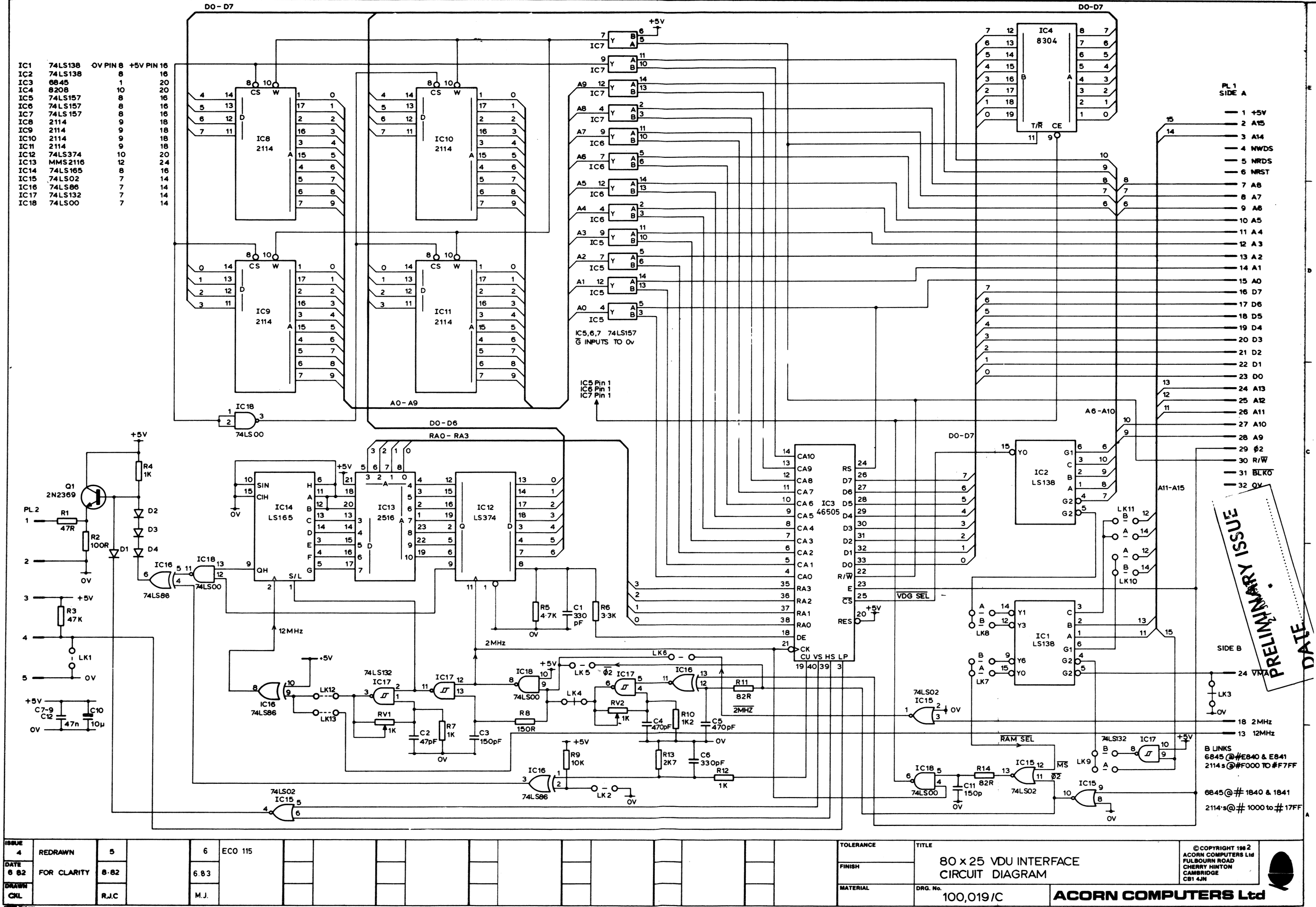
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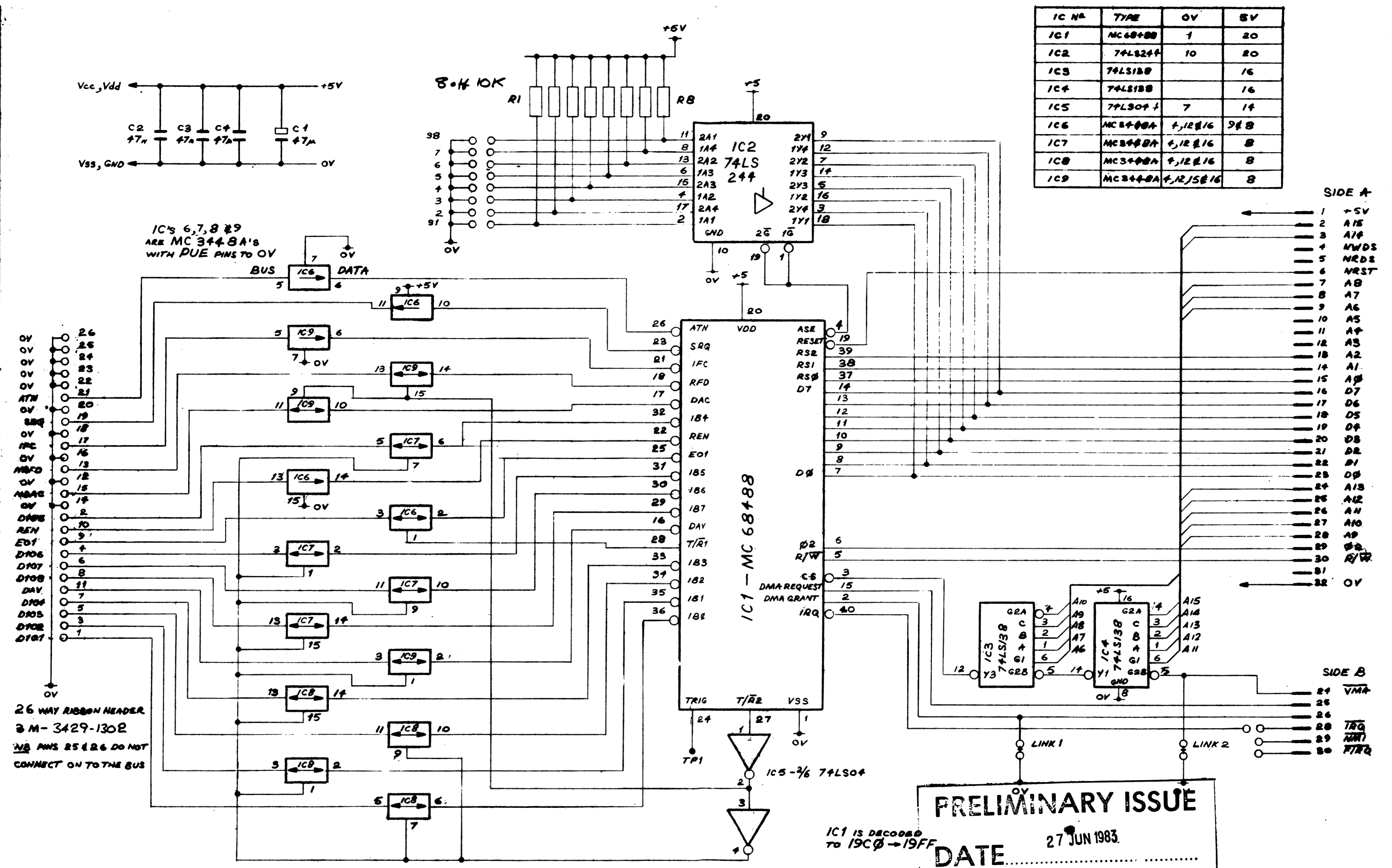
DATE 27 JUN 1983

* 16 RESISTORS NOT. REQD

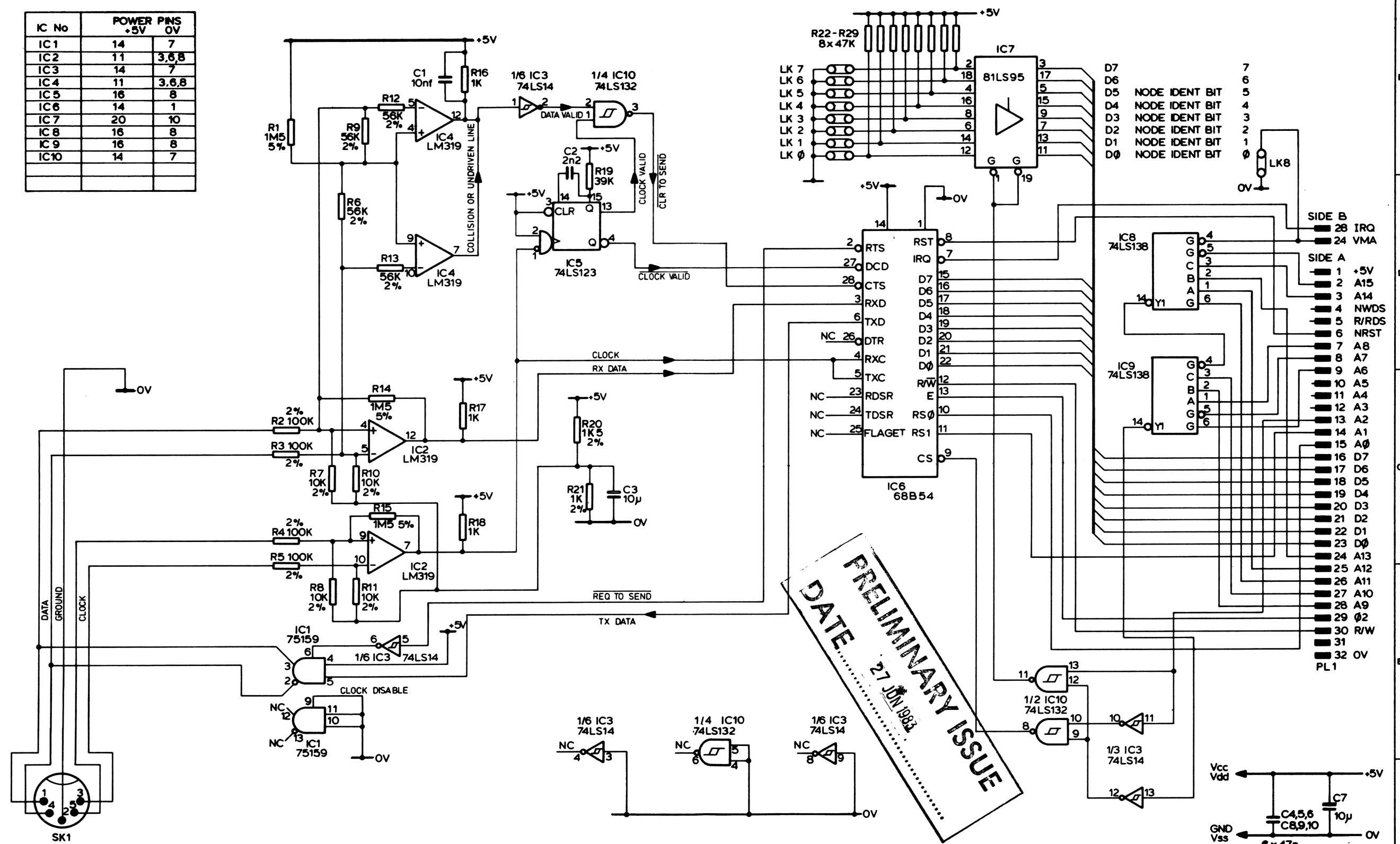
ISSUE	A	1	2		© COPYRIGHT 1980 ACORN COMPUTERS LTD 2A MARKET HILL CAMBRIDGE TEL 0223 312772	DEN GWDS DATE 29-2-80	TITLE CIRCUIT DIAGRAM FOR INTERFACE PANEL PCB	100.017/C ACORN COMPUTERS LTD
DATE	6-3-80	16-4-80	1-8-80					
		CBT	CRT					




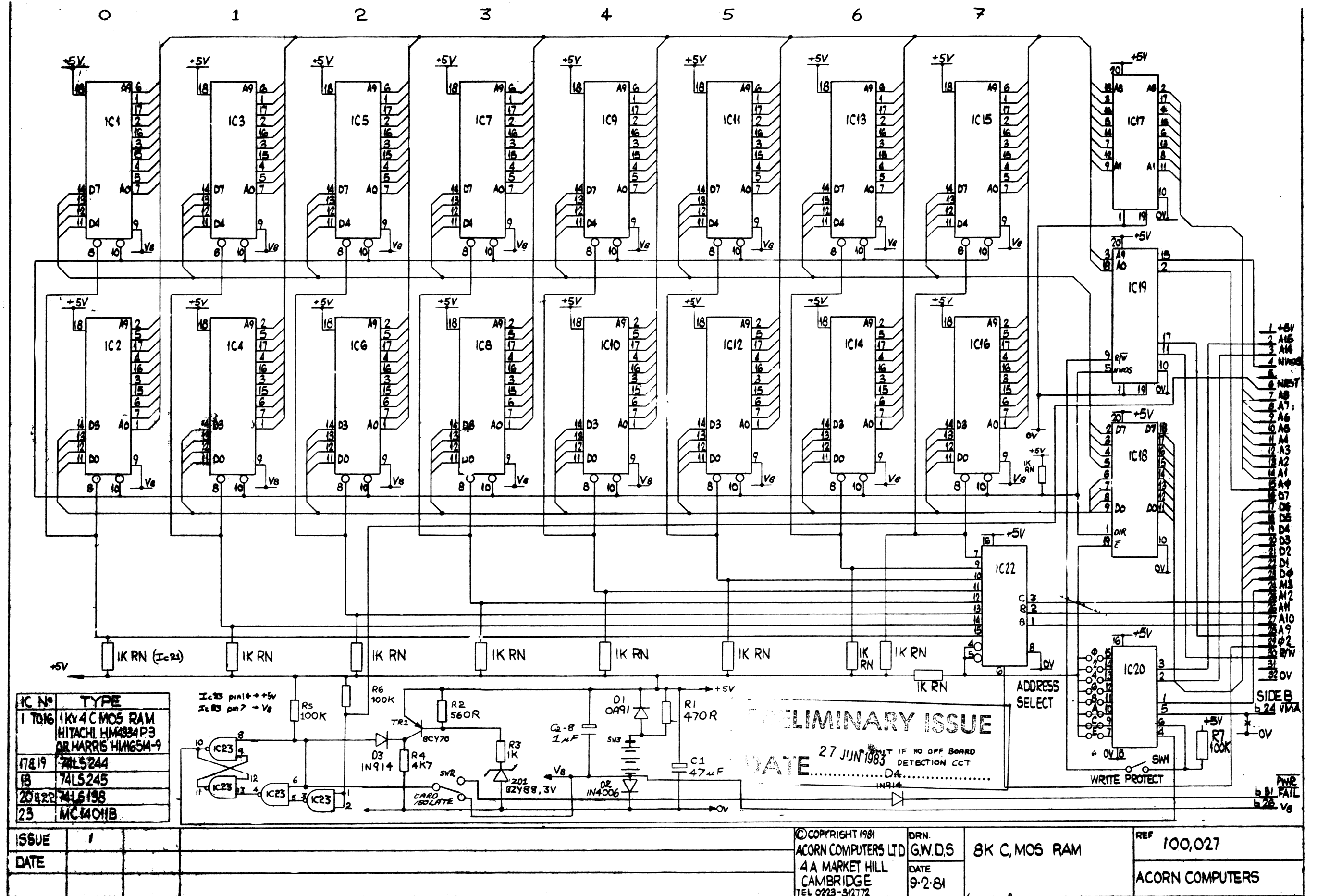


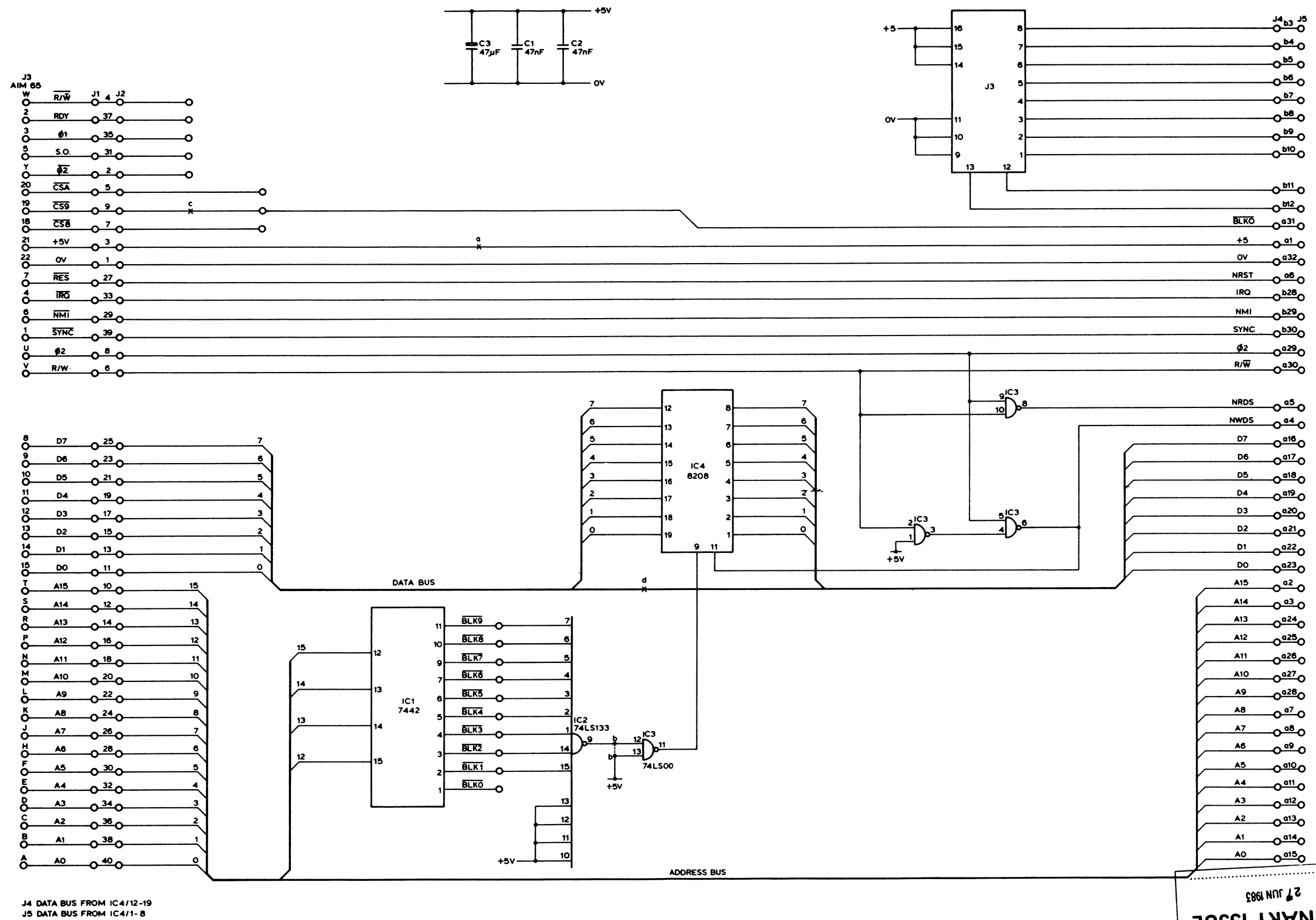


IC No	POWER +5V	PINS OV
IC1	14	7
IC2	11	3,6,8
IC3	14	7
IC4	11	3,6,8
IC5	16	8
IC6	14	1
IC7	20	10
IC8	16	8
IC9	16	8
IC10	14	7



ISSUE - 2	REDRAWN								TOLERANCE	TITLE ECONET NODE CIRCUIT DIAGRAM (EUROCARD VERSION)	©COPYRIGHT 1982 ACORN COMPUTERS Ltd FULBOURN ROAD CHERRY HINTON CAMBRIDGE CB1 4JN	
DATE 7 82									FINISH			
DRAWN R.J.C									MATERIAL	DRG. No. 100,024/C	ACORN COMPUTERS Ltd	





PRELIMINARY ISSUE
DATE 27 JUN 1983

[illegible]